# A General Time-Domain Method for Harmonic Distortion Estimation in CMOS Circuits 

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#### Abstract

This article presents a general, time-domain harmonic distortion estimation method, applicable to linear CMOS circuits characterized by weakly nonlinear behavior, ranging from amplifiers, and transconductors to filters, with any number of stages. It offers a compact, fast, and systematic way to model circuits as a structure of interconnected $\boldsymbol{G}_{\boldsymbol{m}}$-stages, and estimates the harmonic distortion at every circuit node, providing insight into the distortion contribution of every stage. The method uses a more involved model for each $\boldsymbol{G}_{\boldsymbol{m}}$-stage that also accounts for the dependence of its output current on cross-products of its input and output voltages, improving significantly the distortion estimation accuracy. The proposed method is easily implemented in MATLAB, and intends to be integrated as a tool in EDA suites to speed-up distortion estimation. A number of examples are presented, illustrating the application of the method and validating its accuracy via comparison with Cadence Spectre simulation.


Index Terms-CMOS, estimation, $\boldsymbol{G}_{\boldsymbol{m}}$-stage, linear circuits, total harmonic distortion (THD), weak nonlinearities.

## I. Introduction

HARMONIC distortion is an important aspect in characterizing a circuit's performance. Harmonic distortion and noise determine its dynamic range [1], whereas alongside with intermodulation distortion they dictate the circuit's overall linearity. As such, it is a quantity of major impact and interest in a plethora of applications, like audio and power amplifiers [2], radio-frequency amplifiers [3], low-noise amplifiers, filters, and more.

Supposing that a circuit is being driven by a sinusoidal signal of fundamental frequency $\omega$, the total harmonic distortion (THD) at its output is defined as the ratio of the combined power of all generated harmonics to the power of the fundamental

$$
\begin{equation*}
\mathrm{THD}=\sqrt{\frac{\sum_{k=2}^{\infty} V_{h_{k}}^{2}}{V_{f}^{2}} \cdot 100 \% . . . . . . . .} \tag{1}
\end{equation*}
$$

$V_{h_{k}}$ is the (voltage) amplitude of the $k^{\text {th }}$ harmonic, at frequency $k \omega$, and $V_{f}$ is the amplitude of the fundamental tone.

[^0]Realistically, under weakly nonlinear circuit behavior, the harmonic amplitudes decrease rapidly as their order increases, which in turn significantly decreases the number of harmonics needed to have a good estimation of THD. Depending on the specific circuit topology, the dominant harmonic tones are the second and the third harmonic. This raises interest in determining the $\mathrm{HD}_{2}$ and $\mathrm{HD}_{3}$ distortion factors

$$
\begin{equation*}
\mathrm{HD}_{2}=\left|\frac{V_{h_{2}}}{V_{f}}\right|, \quad \mathrm{HD}_{3}=\left|\frac{V_{h_{3}}}{V_{f}}\right| \tag{2}
\end{equation*}
$$

that are the estimation subjects of this article.
Accurate prediction of distortion is, in general, a complicated task. A possible way of estimating the distortion in a circuit is by simulation. A sinusoidal input results in sinusoidal currents and voltages of the same frequency (assuming no chaotic behavior or subharmonic generation), and so simulation methods like harmonic balance or shooting [4], [5], are most appropriate. Unfortunately, the estimation of distortion by such methods is usually time-consuming and computationally expensive [6], does not provide any insight [7], and does not aid with optimization or parameterization of the design.

Dedicated methods that predict the distortion behavior and are easy to use, while being open to parameterization and provide insight are sought after. A popular approach is by means of the Volterra series [8], where the circuit is decomposed into various order operators, each one characterized by a time- or frequency-domain Volterra kernel. Even though the use of the Volterra series can yield quite accurate results, there are shortcomings; the operators' expression and manipulation becomes cumbersome when the number of circuit elements (and thus, distortion contributions) rises, which is almost always the case for practical circuits.

Recently, a methodology utilizing linear-centric circuit models to account for individual distortion contributions in a circuit was reported [9]. Systematic, time-domain state-space harmonic distortion estimation approaches for weakly nonlinear $G_{m}-C$ filters of any order [10]-[12], and a distortion contribution analysis that uses the best linear approximation [13] were also proposed; the latter methodology deviates from the classical distortion point of view and adopts a noise-like analysis, being also capable of handling strong nonlinearities.

Several other methods describe the dominant distortion terms in the frequency-domain [14], [15], where many use algebraic manipulation of simplified amplifier models [16]-[24]. The analysis is mainly focused on the most frequently used amplifier topologies, with one, two, or three stages, employing negative feedback and Miller compensation. Even though some share similarities with the Volterra series approach [25], or adopt them [3], [15], they provide a set of equations ready to be used. However, most works are usually tailored for specific topologies, and may require extensive algebraic manipulation to be applied to more general cases.


Fig. 1. $\quad G_{m}$-stage representation.
Additionally, the models used in many of these approaches could fail to capture faithfully the current-characteristic of a CMOS stage, as will be demonstrated in the sections to follow.

This article extends [26] and proposes a general, timedomain harmonic distortion estimation method for CMOS circuits that exhibit weak nonlinearities. It offers a compact and systematic approach that can be applied to circuit structures ranging from simple transconductors to cascaded amplifiers and filter designs, with any number of stages. It provides a fast and highly accurate estimation of distortion factors $\mathrm{HD}_{2}$ and $\mathrm{HD}_{3}$ by manipulating the circuit under consideration as a structure of interconnected $G_{m}$-stages. Each $G_{m}$-stage is characterized by a more involved model of its currentcharacteristic, whose parameters are extracted via curve-fitting. The method is easily implemented in numerical computing environments like MATLAB, and intends to serve as an integrated tool in dedicated EDA software (like Cadence Spectre) to speed-up distortion estimation.

The remainder of this article is organized as follows. Section II presents the modeling of CMOS stages, and Section III outlines the derivation of the $G_{m}$-stage model coefficients. Section IV introduces the proposed method alongside with examples of its application, while the method's accuracy is validated through simulation results in Section V. Finally, conclusions are drawn in Section VI.

## II. Gm-Stage Modeling

A MOS transistor eventually constitutes a voltage-controlled current source. In the same vein, a CMOS gain stage producing an output current in response to an input voltage can be thought of as a $G_{m}$-stage, a representation of which is depicted in Fig. 1.

Throughout this article, the following notation is used; $G_{i, t, j}^{m}$ is the $G_{m}$-stage with positive input, $u_{i}$, at node $i$; negative input, $k_{i t j} u_{t}$, from node $t$, with $k_{i t j}$ a real feedback factor ${ }^{1}$; and output current, $i_{i t j}$, at node $j$. The stage's differential input voltage is then

$$
\begin{equation*}
\tilde{u}_{i t j}=u_{i}-k_{i t j} u_{t} \tag{3}
\end{equation*}
$$

The $i t j$-triplet ${ }^{2}$ is included in all of the characteristics of a $G_{m}$-stage. The ac-ground is marked as $r$ (reference potential).

For keeping the expressions and notation as simple as possible, single-ended output $G_{m}$-stages were preferred. This, however, does not limit the application of the proposed method, as fully-differential circuits can be easily modeled by single-ended output $G_{m}$-stages, as described later in Section II-D2.

A stage's output current is in general a nonlinear function of its input and output voltages

$$
\begin{equation*}
i_{i t j}=f_{i, t, j}\left(\widetilde{u}_{i t j}, u_{j}\right), \quad f_{i, t, j}: \mathbb{R}^{2} \rightarrow \mathbb{R} \tag{4}
\end{equation*}
$$

This nonlinearity is the cause of distortion generation.

[^1]

Fig. 2. CS amplifier.
Parasitic capacitors in MOS transistors are reported to do not have significant nonlinearity contribution; they reduce the magnitude of the output impedance at high frequencies [27], [28]. This enables the formation of an equivalent model based only on the dc-characteristics of a CMOS stage. Such a model can be obtained by approximating the stage's output current with a power-series expansion around its dc-operating point.

## A. Simple Model

The simplest case results by neglecting the output voltage dependence and modeling the whole stage as a single current source that is governed by power terms of its input voltage

$$
\begin{equation*}
i_{i t j}=\sum_{k \geq 1} g_{i t j, k} \tilde{u}_{i t j}^{k} \tag{5}
\end{equation*}
$$

where $g_{i t j, k}$ is the corresponding coefficient for each power, $k$, of $\tilde{u}_{i t j}^{k}$. This approach, however, will result in poor accuracy since it even fails to account for the finite output impedance of the stage. As such, a better approximation has to be made.

## B. More Accurate Model

The most frequently adopted approach [17], [18], [20]-[23] is to take into account both the input and output voltages and have a power-series approximation of the form

$$
\begin{equation*}
i_{i t j}=\sum_{k \geq 1}\left(g_{i t j, k} \tilde{u}_{i t j}^{k}+\bar{g}_{i t j, k} u_{j}^{k}\right) \tag{6}
\end{equation*}
$$

Since $\mathrm{HD}_{2}$ and $\mathrm{HD}_{3}$ are the dominant distortion factors, one can restrict $k=1,2,3$ to end up with an easy to manipulate and fairly accurate expression. ${ }^{3}$ An additional advantage of this approximation is that the input- and output-related terms are independent of one another; nonetheless, this can lead to significant deviations in the expected behavior of a $G_{m}$-stage.
To illustrate such a case, consider the common-source (CS) amplifier of Fig. 2. Its load, $R_{L} \| C_{L}$, has a reference voltage of $V_{\mathrm{OUT}}^{o p}$, the dc-operating point of the unloaded output.

Should the input and output voltages be independent, the circuit of Fig. 3 must produce the same distortion. The original CS stage is mirrored, isolating the interference between the input and output voltages; $V_{\mathrm{in}}$ acts under a fixed $V_{\mathrm{OUT}}^{o p}$ and produces a current signal, $i_{\text {sig }}$; this signal is in turn injected

[^2]

Fig. 3. CS amplifier-independent input and output voltages.


Fig. 4. $\mathrm{HD}_{2}$ of the two CS amplifier cases.


Fig. 5. $\mathrm{HD}_{3}$ of the two CS amplifier cases.
into the output node of the mirror-stage (operating under $V_{\mathrm{IN}}^{o p}$, the input dc-operating point of Fig. 2), creating $V_{\text {out }}^{\prime}$.

The two cases were simulated by means of Cadence Spectre in TSMC $0.18 \mu \mathrm{~m}$ technology. The amplifier has a dc-gain of 14.7 dB under a load of $10 \mathrm{k} \Omega \| 10 \mathrm{pF}$, and a unitygain frequency of 9.35 MHz . A parametric (with respect to frequency) periodic steady-state (PSS) harmonic balance analysis is performed, with a sinusoidal input signal of 50 mV peak. The resulting $\mathrm{HD}_{2}$ and $\mathrm{HD}_{3}$ are presented in Figs. 4 and 5, respectively. It is evident that the two circuits exhibit different distortion mechanisms, even though they have the same fundamental frequency response. Similar findings on the importance of input-output related terms have been recently reported [15].

## C. Proposed Model

Given the drawbacks of the previous approach, it is fair to say that a suitable model should also include cross-products of the input and output voltages. Such approximations have been


Fig. 6. $\quad$ SF stage as a $G_{m}$-stage.


Fig. 7. CG stage as a $G_{m}$-stage.
used at transistor-level [3], [27]-[30], where the device acting as amplifier is supposed to admit a 2- or 3-D Taylor series expansion; the corresponding coefficients of the approximation are then calculated by the partial derivatives of the transistor's current relationship. Equivalent approaches have been adopted at stage-level [3], [14], [15], [31]-[33].

In this article, each $G_{m}$-stage is considered to have a timedomain [10]-[12], [34] power-series current expression

$$
\begin{equation*}
i_{i t j}=\sum_{\substack{k, \ell \geq 0 \\ k+\ell \geq 1}} g_{i t j}^{k \ell} \widetilde{u}_{i t j}^{k} u_{j}^{\ell} \tag{7}
\end{equation*}
$$

Note that for notation simplicity, the $k \ell$-superscript in the $g_{i t j}^{k \ell}{ }^{-}$ coefficients indicates the corresponding power of $\widetilde{u}_{i t j}^{k}$ and $u_{j}^{\ell}$, and not a power term of the coefficient itself.

It is chosen that $k+\ell=1,2,3$, to capture the dominant contributing terms to the fundamental, second and third harmonic, and (7) reduces to

$$
\begin{align*}
i_{i t j}= & g_{i t j}^{10} \widetilde{u}_{i t j}+g_{i t j}^{20} \widetilde{u}_{i t j}^{2}+g_{i t j}^{30} \widetilde{u}_{i t j}^{3} \\
& +g_{i t j}^{01} u_{j}+g_{i t j}^{02} u_{j}^{2}+g_{i t j}^{03} u_{j}^{3} \\
& +g_{i t j}^{11} \widetilde{u}_{i j j} u_{j}+g_{i t j}^{21} \widetilde{u}_{i t j}^{2} u_{j}+g_{i t j}^{12} \widetilde{u}_{i t j} u_{j}^{2} \tag{8}
\end{align*}
$$

The $g_{i t j}^{k \ell}$-coefficients of (8) of each $G_{m}$-stage are derived by curve-fitting to capture in detail its amplitude-adjusted nonlinearities. Taylor series expansion at the dc-operating point (used in most of the aforementioned works and employed in various analyses) is accurate only locally for small signal amplitudes [35]. The derivation procedure of the $g_{i t j}^{k l}$-coefficients is described analytically in Section III.

## D. Formulation of Standard CMOS Gain Stages

The formulation of a CMOS gain stage depends on its topology, and thus, whether it is single-ended or fully-differential.

1) Single-Ended Structures: The differential-pair can be modeled directly in the form of Fig. 1. For a CS stage, the negative input should be ac-grounded; a source-follower (SF) stage should be treated as a $G_{m}$-stage with $100 \%$ negative feedback $\left(k_{i j j}=1\right)$, as shown in Fig. 6.

A common-gate (CG) stage is modeled as depicted in Fig. 7, where antiparallel stages $G_{i, r, j}^{m}$ and $G_{r, i, i}^{m}$ have the same $g_{i t j}^{k \ell}$, coefficients. This formulation results from the fact that in a CG stage the output current flows toward its input source.


Fig. 8. Fully-differential structure $G_{m}$-stage representation.


Fig. 9. Three-stage feedback amplifier.

Stages that result from combinations of the aforementioned standard ones can be represented as connections of their $G_{m}$ stage equivalents, or even be treated as a single $G_{m}$-stage. An example of the latter case is presented in Section IV-E1.
2) Fully-Differential Structures: In the case of a fullydifferential stage, an appropriate representation would be that of Fig. 8. The two $G_{m}$-stages are connected with their inputs in parallel, and each set of $g_{i t j}^{k \ell}$-coefficients results from the common input and the corresponding output. Ideally, $g_{i t m}^{k \ell}=-g_{i t j}^{k \ell}$, so as to achieve $i_{i t m}=-i_{i t j}$.

## E. Circuit Transformation Into a $G_{m}$-Stage Equivalent

The transformation of a circuit into an equivalent representation of $G_{m}$-stages relies on identifying the standard CMOS gain stages that it is comprised of, and their interconnections.

As an example, consider the three-stage feedback amplifier of Fig. 9. The amplifier consists of three cascaded stages (differential-pair, CS, SF) and a buffer stage (SF) for Miller compensation.

The equivalent representation of the amplifier is that of Fig. 10. Transistors $M_{0}-M_{4}$ of the differential-pair form the first $G_{m}$-stage, $G_{0,3,1}^{m}$, while the CS stage of $M_{5}-M_{6}$ forms the second stage, $G_{1, r, 2}^{m}$. The SF stage of $M_{7}-M_{8}$ forms $G_{2,3,3}^{m}$, and the SF stage of $M_{9}-M_{10}$ forms $G_{2,4,4}^{m}$. Moreover, it is $k_{233}=k_{244}=1$, as described in Section II-D1.

The negative input of the differential-pair (and thus, $G_{0,3,1}^{m}$ ) is fed with an ac-voltage of

$$
\frac{R_{L_{2}}}{R_{L_{1}}+R_{L_{2}}} u_{\text {out }}=k_{031} u_{\text {out }}=k_{031} u_{3}
$$

$\underset{\sim}{\text { where }} R_{L_{1}}+R_{L_{2}}=R_{L}=R_{3}$. Finally, $C_{L}=C_{3}$ and $C_{C}=$ $\widetilde{C}_{14}$. The ac-input signal, $u_{i n}=u_{0}$, is realized by the current


Fig. 10. $G_{m}$-stage equivalent representation of the three-stage feedback amplifier.


Fig. 11. Closed-loop system configuration.
source $\hat{i}_{0}=u_{0} / R_{0}$ that acts on the normalized ${ }^{4} R_{0}=1 \Omega$; this transformation results from the method's formulation that is presented in Section IV.

## III. Model Coefficients Derivation

The derivation of the $g_{i t j}^{k \ell}$-coefficients of each $G_{m}$-stage model can be performed by a three-step procedure that involves an ac-analysis performed to the whole circuit structure, 2-D dc-sweeps around the input and output dc-operating points of each stage, and, finally, linear regression for the estimation of the coefficients. The findings of this procedure should confirm weakly nonlinear behavior for all $G_{m}$-stages, otherwise the method's accuracy will be degraded.

## A. AC-Analysis for Amplitude Levels Estimation

An ac-analysis is initially performed to the complete circuit under consideration, to gain knowledge about the expected signal amplitude at the input and the output of each stage.

The input and output amplitudes of a stage with respect to the circuit's input signal can deviate from an anticipated monotonous drop in open-loop applications as frequency rises. When the circuit is in closed-loop configuration, the maximum value may be achieved at higher frequencies. In the system representation of Fig. 11, the gain from the input signal, $u_{s}$, to the input of the first stage, $u_{1}$, is

$$
\begin{equation*}
\frac{u_{1}}{u_{s}}=\frac{1}{1+\beta \alpha_{1} \cdots \alpha_{n}} \tag{9}
\end{equation*}
$$

As the gain of each stage falls with frequency, the gain to the input of the first stage rises, and so does its input amplitude. Depending on the gains $\alpha_{1}, \ldots, \alpha_{n}$, and whether additional feedback exists, more stages can exhibit similar behavior.
Figs. 12 and 13 depict the gains $u_{1} / u_{s}, u_{2} / u_{s}$, and $u_{3} / u_{s}$, for the differential-pair and the CS stage, respectively, of the feedback amplifier of Fig. 9. Let the differential-pair's differential input voltage be denoted by $u_{1}$ and its output voltage

[^3]

Fig. 12. Gains $u_{1} / u_{s}$ and $u_{2} / u_{s}$ for the differential-pair.


Fig. 13. Gains $u_{2} / u_{s}$ and $u_{3} / u_{s}$ for the CS stage.
by $u_{2}$; accordingly, the CS stage has $u_{2}$ as input voltage and $u_{3}$ as output voltage. Whereas both gains reach their maximum value at low frequencies in the CS stage, the gain of the input of the differential-pair starts to rise and reaches its peak higher in frequency, but well inside the amplifier's unity-gain bandwidth of 16.80 MHz . The simulations were done again with Cadence Spectre in TSMC $0.18 \mu \mathrm{~m}$ technology.

## B. 2-D DC-Sweeps for Current-Characteristics

The derived maximum values (within the bandwidth of interest for the distortion estimation), when referred back to the excitation signal, mark the peak amplitudes that the input and the output of a stage will confront. As such, they are the ranges for the 2-D dc-sweep required to capture the dependence of the stage's output current on its input and output voltages.

Each stage is set to its input and output dc-operating points, and a 2-D dc-sweep of its output current is preformed under no load; the two sweeping parameters are the input and output voltages, with $r_{1}$ and $r_{2}$ number of steps, respectively.

## C. Model Coefficients Extraction via Linear Regression

After all 2-D dc-sweeps are completed, the acquired data have to be processed in an appropriate way to derive each stage's $g_{i t j}^{k \ell}$-coefficients. A linear regression approach is used, in the form of a linear least-squares problem [36]

$$
\begin{equation*}
I_{g}=U G+I_{\varepsilon} \tag{10}
\end{equation*}
$$

The matrices $U \in \mathbb{R}^{\left(r_{1} \cdot r_{2}\right) \times 9}$ and $G \in \mathbb{R}^{9 \times 1}$ are defined as

$$
U=\left[\begin{array}{llllll}
\widetilde{u}_{i t j}, & \widetilde{u}_{i t j}^{2}, & \widetilde{u}_{i t j}^{3}, & u_{j}, & u_{j}^{2}, & u_{j}^{3},  \tag{11}\\
\widetilde{u}_{i t j} u_{j}, & \widetilde{u}_{i t j}^{2} u_{j}, & \widetilde{u}_{i t j} u_{j}^{2}
\end{array}\right]
$$

$$
\begin{equation*}
G=\left[g_{i t j}^{10}, \quad g_{i t j}^{20}, \quad g_{i t j}^{30}, \quad g_{i t j}^{01}, \quad g_{i t j}^{02}, \quad g_{i t j}^{03}, \quad g_{i t j}^{11}, \quad g_{i t j}^{21}, g_{i t j}^{12}\right]^{\mathrm{T}} \tag{12}
\end{equation*}
$$

forming (8) for all the values of the stage's output current, embodied in $I_{g} \in \mathbb{R}^{\left(r_{1} \cdot r_{2}\right) \times 1}$. The column matrix $I_{\varepsilon} \in \mathbb{R}^{\left(r_{1} \cdot r_{2}\right) \times 1}$ accounts for the error between the current values obtained by simulation $\left(I_{g}\right)$ and the ones resulting from $U G$.

For normalization purposes, and to ensure a robust solution, a scaling is introduced in (10) by means of a diagonal matrix, $D$

$$
\begin{align*}
& I_{g}=U D^{-1} D G+I_{\varepsilon}  \tag{13}\\
& D=\operatorname{diag}\left(\delta_{1}, \ldots, \delta_{9}\right) \in \mathbb{R}^{9 \times 9} \tag{14}
\end{align*}
$$

where $\delta_{i}$ is the Euclidean norm [37] of the $i^{\text {th }}$ column of $U$. The solution yielding the estimation of the $g_{i t j}^{k \ell}$-coefficients is

$$
\begin{equation*}
G=D^{-1}\left[\left(U D^{-1}\right)^{\mathrm{T}} U D^{-1}\right]^{-1}\left(U D^{-1}\right)^{\mathrm{T}} I_{g} \tag{15}
\end{equation*}
$$

## D. Weakly Nonlinear $G_{m}$-Stage Behavior Criteria

Although it is not always trivial to confirm that a circuit operates under weak nonlinearities a priori [8], it is possible to practically confirm such an operation, subjected to certain bounds. Useful insight can be gained by the following two criteria.

1) Criterion 1: First, the accuracy of the least-squares fit to the simulation current values of a $G_{m}$-stage is evaluated by the bounded ratio

$$
\begin{equation*}
\frac{\left\|I_{\varepsilon}\right\|_{1}}{\left\|I_{g}\right\|_{1}}=\frac{\left\|I_{g}-U G\right\|_{1}}{\left\|I_{g}\right\|_{1}} \leq b_{I} \tag{16}
\end{equation*}
$$

where $\|\cdot\|_{1}$ denotes the 1 -norm [37], and $b_{I}>0$.
This ratio must be small in order to confirm a good fit. As such, the value of $b_{I}$ can be adjusted to meet the desired level of accuracy. In the example cases of Section V , a value of $b_{I}$ in the order of $10^{-4}$ was found to yield accurate fitting.
2) Criterion 2: After a fit with good accuracy is confirmed, the normalized magnitudes of the nonlinear terms of (8) are considered. Let

$$
\begin{align*}
& E\left(\widetilde{u}_{i t j}, u_{j}\right)=\left[\begin{array}{ccc}
\left|g_{i t j}^{10} \widetilde{u}_{i t j}\right| & \left|g_{i t j}^{20} \widetilde{u}_{i t j}^{2}\right| & \left|g_{i t j}^{30 \widetilde{u}_{i t j}^{3}}\right| \\
\left|g_{i t j}^{01} u_{j}\right| & \left|g_{i t j}^{02} u_{j}^{2}\right| & \left|g_{i t j}^{03} u_{j}^{3}\right| \\
\left|g_{i t j}^{1} \tilde{u}_{i t j} u_{j}\right| & \left|g_{i t j}^{2} \widetilde{u}_{i t j}^{2} u_{j}\right| & \left|g_{i t j}^{12} \widetilde{u}_{i t j} u_{j}^{2}\right|
\end{array}\right] \in \mathbb{R}^{3 \times 3} \\
& \bar{E}=\frac{1}{\left|g_{i t j}^{10} \tilde{u}_{i t j}^{\max }\right|} E\left(\widetilde{u}_{i t j}^{\max }, u_{j}^{\max }\right) \in \mathbb{R}^{3 \times 3} \tag{17}
\end{align*}
$$

where it is assumed that the dominant linear term of (8) is $g_{i t j}^{10} \widetilde{u}_{i t j}$, and the maximum values $\widetilde{u}_{i t j}^{\max }$ and $u_{j}^{\max }$ are taken from the 2-D dc-sweep ranges of the $G_{m}$-stage.

The entries of $\bar{E}$ that correspond to the normalized nonlinear terms of (8) can be bounded by $b_{g}^{n \ell}>0$, to ensure that the dominant linear term, $g_{i t j}^{10} \tilde{u}_{i t j}$, is at least $1 / b_{g}^{n \ell}$ times stronger over the entire input and output voltage ranges. A value of $b_{g}^{n \ell}$ in the order of $10^{-1}$ was found to be reasonable, especially for a stage where $u_{j}^{\max } \gg \widetilde{u}_{i t j}^{\max }$.


Fig. 14. General circuit structure, composed of $G_{m}$-stages.

## IV. Harmonic Distortion Estimation

Consider the general circuit structure of Fig. 14. The topology is composed of $G_{m}$-stages, resistors and capacitors. Each node $j, j=0,1, \ldots, n$, may feature a resistor, $R_{j}$, and a capacitor, $C_{j}$, connected to ground. It can also have an excitation signal, as an independent current source, $\hat{i}_{j}$. The ${\underset{\sim}{C}}^{\sim} G_{m}$-stages behave as described in Section II, while capacitor $\widetilde{C}_{\ell j}$ provides coupling between nodes $\ell$ and $j$, with

$$
\begin{equation*}
i_{\widetilde{C}_{\ell j}}=\widetilde{C}_{\ell j}\left(\dot{u}_{\ell}-\dot{u}_{j}\right)=-i_{\widetilde{C}_{j \ell}} \tag{19}
\end{equation*}
$$

For each node $j$, it holds

$$
\begin{equation*}
\hat{i}_{j}+\sum_{i, t} i_{i t j}+\sum_{\ell} i \widetilde{c}_{\ell j}=\frac{u_{j}}{R_{j}}+C_{j} \dot{u}_{j} \tag{20}
\end{equation*}
$$

and by using (19)

$$
\begin{equation*}
\hat{i}_{j}+\sum_{i, t} i_{i t j}+\sum_{\ell} \widetilde{C}_{\ell j} \dot{u}_{\ell}=\frac{u_{j}}{R_{j}}+\left(C_{j}+\sum_{\ell} \widetilde{C}_{\ell j}\right) \dot{u}_{j} . \tag{21}
\end{equation*}
$$

Equation (21) constitutes a manipulated form of the statespace equation of the general form $C^{c} \dot{u}=G^{c} u+\hat{I}$, where $C^{c}$ and $G^{c}$ are the corresponding capacitance and conductance matrices of the whole circuit.

Assuming that the circuit operates in steady-state, let the voltage of node $j$ be of the form (recall that only harmonics up to the third are considered)

$$
\begin{align*}
u_{j} & =u_{j}^{f}+u_{j}^{h}  \tag{22}\\
u_{j}^{f} & =\theta^{f} S_{j}^{f}  \tag{22a}\\
u_{j}^{h} & =\theta^{h} S_{j}^{h} \tag{22b}
\end{align*}
$$

where ${ }^{5}$

$$
\begin{align*}
S_{j}^{f} & =\left[\begin{array}{ll}
a_{j, 1}, & b_{j, 1}
\end{array}\right]^{\mathrm{T}} \in \mathbb{R}^{2 \times 1}  \tag{23}\\
S_{j}^{h} & =\left[\begin{array}{ll}
a_{j, 2}, & b_{j, 2}, \\
a_{j, 3}, & b_{j, 3}
\end{array}\right]^{\mathrm{T}} \in \mathbb{R}^{4 \times 1}  \tag{24}\\
\theta^{f} & =\left[\begin{array}{ll}
\sin \omega t, & \cos \omega t
\end{array}\right] \in \mathbb{R}^{1 \times 2}  \tag{25}\\
\theta^{h} & =\left[\begin{array}{lll}
\sin 2 \omega t, & \cos 2 \omega t, \quad \sin 3 \omega t, & \cos 3 \omega t
\end{array}\right] \in \mathbb{R}^{1 \times 4} . \tag{26}
\end{align*}
$$

[^4]Terms $u_{j}^{f}$ and $u_{j}^{h}$ represent the voltage components of the fundamental and the harmonic tones, respectively, while vectors $S_{j}^{f}$ and $S_{j}^{h}$ feature the corresponding $\sin$ and cos coefficients.
In the same vein, for the excitation current source of node $j$ it is

$$
\begin{align*}
\hat{i}_{j} & =\hat{i}_{j}^{f}+\hat{i}_{j}^{h}  \tag{27}\\
\hat{i}_{j}^{f} & =\theta^{f} P_{j}^{f}  \tag{27a}\\
\hat{i}_{j}^{h} & =\theta^{h} P_{j}^{h} \tag{27b}
\end{align*}
$$

where

$$
\begin{align*}
P_{j}^{f} & =\left[\begin{array}{ll}
\hat{a}_{j, 1}, & \hat{b}_{j, 1}
\end{array}\right]^{\mathrm{T}} \in \mathbb{R}^{2 \times 1}  \tag{28}\\
P_{j}^{h} & =\left[\begin{array}{lll}
\hat{a}_{j, 2}, & \hat{b}_{j, 2}, & \hat{a}_{j, 3}, \\
\hat{b}_{j, 3}
\end{array}\right]^{\mathrm{T}} \in \mathbb{R}^{4 \times 1} . \tag{29}
\end{align*}
$$

In the following analysis, it has been assumed that the circuit features only one excitation source, that of node 0 .

The proposed harmonic distortion estimation method is performed in two steps. First, the fundamental tone of $\omega$ is estimated, followed by the estimation of the harmonic tones of $2 \omega$ and $3 \omega$, to finally derive $\mathrm{HD}_{2}$ and $\mathrm{HD}_{3}$.

## A. Fundamental Tone Estimation

Since weak nonlinearities are assumed, their contribution to the fundamental tone can be considered negligible. Thus, the fundamental tone voltage component of node $j$, (22a), should satisfy the linear part of (21). As such, for the fundamental tone estimation, the output current, $i_{i t j}^{f}$, of stage $G_{i, t, j}^{m}$, is expressed using (3) and (22a) as only the linear part of (8)

$$
\begin{equation*}
i_{i t j}^{f}=\theta^{f}\left(g_{i t j}^{10} S_{i}^{f}-g_{i t j}^{10} k_{i t j} S_{t}^{f}+g_{i t j}^{01} S_{j}^{f}\right) \tag{30}
\end{equation*}
$$

That is, a $G_{m}$-stage's output current is the sum of three independent ones, each one linearly dependent on the fundamental tone voltage component of its corresponding node.
The expression of $i_{i t j}^{f}$ can be used to form a system of equations for the fundamental tone coefficients of the complete circuit under consideration, relying on (21). Let

$$
J=\left[\begin{array}{cc}
0 & -1  \tag{31}\\
1 & 0
\end{array}\right] \in \mathbb{R}^{2 \times 2}
$$

Then, it is

$$
\begin{equation*}
\dot{u}_{j}^{f}=\dot{\theta}^{f} S_{j}^{f}=\omega \theta^{f} J S_{j}^{f} \tag{32}
\end{equation*}
$$

Combining (30) and (32) with (21) results in

$$
\begin{align*}
& \theta^{f}\left[P_{j}^{f}+\sum_{i, t}\left(g_{i t j}^{10} S_{i}^{f}-g_{i t j}^{10} k_{i t j} S_{t}^{f}+g_{i t j}^{01} S_{j}^{f}\right)+\sum_{\ell} \widetilde{C}_{\ell j} \omega J S_{\ell}^{f}\right] \\
& \quad=\theta^{f}\left[\frac{1}{R_{j}} S_{j}^{f}+\left(C_{j}+\sum_{\ell} \widetilde{C}_{\ell j}\right) \omega J S_{j}^{f}\right] \tag{33}
\end{align*}
$$

The vector function $\theta^{f}$ consists of two linearly independent functions of time, and embodies two independent equations with respect to the coefficients of $\sin \omega t$ and $\cos \omega t$. So, $\theta^{f}$ can be eliminated for (33) to hold for any $t \in \mathbb{R}$. Reordering terms yields

$$
\begin{align*}
P_{j}^{f} & +\sum_{i, t} g_{i t j}^{10} S_{i}^{f}-\sum_{i, t} g_{i t j}^{10} k_{i t j} S_{t}^{f}+\omega \sum_{\ell} \widetilde{C}_{\ell j} J S_{\ell}^{f} \\
& =\left(\frac{1}{R_{j}}-\sum_{i, t} g_{i t j}^{01}\right) S_{j}^{f}+\omega\left(C_{j}+\sum_{\ell} \widetilde{C}_{\ell j}\right) J S_{j}^{f} \tag{34}
\end{align*}
$$

Consider the vector of the fundamental tone coefficients of all voltages

$$
S^{f}=\left[\begin{array}{llll}
\left(S_{0}^{f}\right)^{\mathrm{T}}, & \left.\left(S_{1}^{f}\right)^{\mathrm{T}}, \quad \ldots, \quad\left(S_{n}^{f}\right)^{\mathrm{T}}\right]^{\mathrm{T}} \in \mathbb{R}^{2(n+1) \times 1}, ~ \tag{35}
\end{array}\right.
$$

and that of the excitation current source ${ }^{6}$

$$
P^{f}=\left[\left(P_{0}^{f}\right)^{\mathrm{T}}, \quad\left(\begin{array}{ll}
0, & 0 \tag{36}
\end{array}\right), \quad \ldots, \quad(0,0)\right]^{\mathrm{T}} \in \mathbb{R}^{2(n+1) \times 1}
$$

Then, the equivalent of (34) for the whole circuit structure is constructed in a block-matrix form

$$
\begin{equation*}
P^{f}+\left(G^{f}+K^{f}+\omega F^{f}\right) S^{f}=\left(T^{f}+\omega W^{f}\right) S^{f} \tag{37}
\end{equation*}
$$

where

$$
\begin{align*}
G^{f} & =\left[\sum_{t} g_{i t j}^{10}\right]_{j, i=0}^{n} \otimes I_{2} \in \mathbb{R}^{2(n+1) \times 2(n+1)}  \tag{38}\\
K^{f} & =\left[-\sum_{i} g_{i t j}^{10} k_{i t j}\right]_{j, t=0}^{n} \otimes I_{2} \in \mathbb{R}^{2(n+1) \times 2(n+1)}  \tag{39}\\
F^{f} & =\left[\widetilde{C}_{\ell j}\right]_{j, \ell=0}^{n} \otimes J \in \mathbb{R}^{2(n+1) \times 2(n+1)}  \tag{40}\\
T^{f} & =\operatorname{diag}\left(\left[\frac{1}{R_{j}}-\sum_{i, t} g_{i t j}^{01}\right]_{j=0}^{n}\right) \otimes I_{2} \in \mathbb{R}^{2(n+1) \times 2(n+1)}  \tag{41}\\
W^{f} & =\operatorname{diag}\left(\left[C_{j}+\sum_{\ell} \widetilde{C}_{\ell j}\right]_{j=0}^{n}\right) \otimes J \in \mathbb{R}^{2(n+1) \times 2(n+1)} . \tag{42}
\end{align*}
$$

In (38)-(42), $\otimes$ denotes the Kronecker's product [37], and $I_{n}$ is the $n \times n$ identity matrix. The vector of the fundamental tone coefficients is the solution of (37)

$$
\begin{equation*}
S^{f}=\left[T^{f}-G^{f}-K^{f}+\omega\left(W^{f}-F^{f}\right)\right]^{-1} P^{f} \tag{43}
\end{equation*}
$$

[^5]
## B. Harmonic Tones Estimation

The harmonic tones voltage components of node $j$, (22b), are generated by the nonlinear terms of (8). Including all voltage terms in the form of (22) and computing all produced terms would result in cumbersome expressions that constitute a nonlinear equality problem. Even though this approach yields the exact algebraic solution, it is neither easy to implement nor computationally efficient.

A close inspection of (8), in association with (22), reveals that the generated coefficients of the second and third harmonic tones due to the nonlinear terms of (8) will eventually be a sum of products of the following.
a) Only fundamental tone coefficients.
b) A single harmonic tone coefficient to the power of one and one or more fundamental tone coefficients.
c) Higher orders or products of harmonic tones coefficients.

Since the amplitudes of the harmonic tones are expected to be much smaller than that of the fundamental tone, products involving two or more harmonic tones coefficients or powers of them are negligible and can be safely ignored. This approximation can be validated after the estimation of the harmonic tones, as demonstrated in Section IV-C.

Based on the above and including the linear part of (8), the output current, $i_{i t j}^{h}$, of stage $G_{i, t, j}^{m}$, can be approximated, using (3) and (22b), by

$$
\begin{gather*}
i_{i t j}^{h}=\theta^{h}\left[g_{i t j}^{10} S_{i}^{h}-g_{i t j}^{10} k_{i t j} S_{t}^{h}+g_{i t j}^{01} S_{j}^{h}+Z_{i t j}\right. \\
\left.+X_{i t j}^{\alpha} S_{i}^{h}+X_{i t j}^{\beta} S_{t}^{h}+X_{i t j}^{\gamma} S_{j}^{h}\right] \tag{44}
\end{gather*}
$$

where the term $\theta^{h} Z_{i t j}$ is the sum of the products a), and the term $\theta^{h}\left(X_{i t j}^{\alpha} S_{i}^{h}+X_{i t j}^{\beta} S_{t}^{h}+X_{i t j}^{\gamma} S_{j}^{h}\right)$ is the sum of the products b). Note that the approximate expression of $i_{i t j}^{h}$ is linear to the corresponding harmonic tones coefficients.
The term $Z_{i t j}$ is expressed as

$$
\begin{equation*}
Z_{i t j}=\left[p_{i t j}^{s 2}, \quad p_{i t j}^{c 2}, \quad p_{i t j}^{s 3}, \quad p_{i t j}^{c 3}\right]^{\mathrm{T}} \in \mathbb{R}^{4 \times 1} \tag{45}
\end{equation*}
$$

where

$$
\begin{align*}
p_{i t j}^{s 2} & =g_{i t j}^{20} f_{i j}^{s 2}+g_{i t j}^{02} f_{j}^{s 2}+g_{i t j}^{11} h_{i t j}^{s 2}  \tag{46}\\
p_{i t j}^{c 2} & =g_{i t j}^{20} f_{i t j}^{c 2}+g_{i t j}^{02} f_{j}^{c 2}+g_{i t j}^{11} h_{i t j}^{c 2}  \tag{47}\\
p_{i t j}^{s 3} & =g_{i t i j}^{30} j_{i t j}^{s 3}+g_{i t j}^{03} f_{j}^{33}+g_{i t j}^{21} r_{i j}^{s 3}+g_{i t j}^{12} o_{i t j}^{s 3}  \tag{48}\\
p_{i t j}^{c 3} & =g_{i t j}^{30} f_{i t j}^{c 3}+g_{i t j}^{03} f_{j}^{c 3}+g_{i t j}^{21} r_{i t j}^{c 3}+g_{i t j}^{12} c_{i t j}^{c 3} . \tag{49}
\end{align*}
$$

Furthermore, matrices $X_{i t j}^{\alpha} \in \mathbb{R}^{4 \times 4}, X_{i t j}^{\beta} \in \mathbb{R}^{4 \times 4}$, and $X_{i t j}^{\gamma} \in$ $\mathbb{R}^{4 \times 4}$ are defined as

$$
\begin{align*}
& X_{i t j}^{\alpha}=g_{i t j}^{20} \widetilde{N}_{i t j}+g_{i t j}^{30} \frac{3}{2} \widetilde{M}_{i t j}+g_{i t j}^{11} \frac{1}{2} N_{j}+g_{i t j}^{21} Q_{i t j}+g_{i t j}^{12} \frac{1}{2} M_{j} \\
& X_{i t j}^{\beta}=-k_{i t j} X_{i t j}^{\alpha}  \tag{50}\\
& X_{i t j}^{\gamma}=g_{i t j}^{02} N_{j}+g_{i t j}^{03} \frac{3}{2} M_{j}+g_{i t j}^{11} \frac{1}{2} \widetilde{N}_{i t j}+g_{i t j}^{21} \frac{1}{2} \widetilde{M}_{i t j}+g_{i t j}^{12} Q_{i t j} . \tag{51}
\end{align*}
$$

The quantities forming (46)-(52) can be found in the Appendix and are omitted from this part of the text for better comprehension of the method's steps.

Expression (44) is now used to form a system of equations for the harmonic tones coefficients, based again on (21). To
this end, let

$$
L=\left[\begin{array}{ll}
2 & 0  \tag{53}\\
0 & 3
\end{array}\right] \in \mathbb{R}^{2 \times 2}
$$

and express

$$
\begin{equation*}
\dot{u}_{j}^{h}=\dot{\theta}^{h} S_{j}^{h}=\omega \theta^{h}(L \otimes J) S_{j}^{h} \tag{54}
\end{equation*}
$$

The combination of (44) and (54) with (21) yields

$$
\begin{align*}
\theta^{h}\left[P_{j}^{h}\right. & +\sum_{i, t}\left(g_{i t j}^{10} S_{i}^{h}-g_{i t j}^{10} k_{i t j} S_{t}^{h}+g_{i t j}^{01} S_{j}^{h}+Z_{i t j}\right. \\
& \left.\left.+X_{i t j}^{\alpha} S_{i}^{h}+X_{i t j}^{\beta} S_{t}^{h}+X_{i t j}^{\gamma} S_{j}^{h}\right)+\sum_{\ell} \widetilde{C}_{\ell j} \omega(L \otimes J) S_{\ell}^{h}\right] \\
= & \theta^{h}\left[\frac{1}{R_{j}} S_{j}^{h}+\left(C_{j}+\sum_{\ell} \widetilde{C}_{\ell j}\right) \omega(L \otimes J) S_{j}^{h}\right] \tag{55}
\end{align*}
$$

The vector function $\theta^{h}$ consists of four linearly independent functions of time, implying four equations of the coefficients of $\sin 2 \omega t$, $\cos 2 \omega t, \sin 3 \omega t$, and $\cos 3 \omega t$. Eliminating $\theta^{h}$ and reordering terms results in

$$
\begin{align*}
& \left(P_{j}^{h}+\sum_{i, t} Z_{i t j}\right)+\sum_{i, t} g_{i t j}^{10} S_{i}^{h}-\sum_{i, t} g_{i t j}^{10} k_{i t j} S_{t}^{h} \\
& +\sum_{i, t}\left(X_{i t j}^{\alpha} S_{i}^{h}+X_{i t j}^{\beta} S_{t}^{h}+X_{i t j}^{\gamma} S_{j}^{h}\right)+\omega \sum_{\ell} \widetilde{C}_{\ell j}(L \otimes J) S_{\ell}^{h} \\
& =\left(\frac{1}{R_{j}}-\sum_{i, t} g_{i t j}^{01}\right) S_{j}^{h}+\omega\left(C_{j}+\sum_{\ell} \widetilde{C}_{\ell j}\right)(L \otimes J) S_{j}^{h} . \tag{56}
\end{align*}
$$

Let the vector of the harmonic tones coefficients of all voltages of the circuit be

$$
\begin{equation*}
S^{h}=\left[\left(S_{0}^{h}\right)^{\mathrm{T}}, \quad\left(S_{1}^{h}\right)^{\mathrm{T}}, \quad \ldots, \quad\left(S_{n}^{h}\right)^{\mathrm{T}}\right]^{\mathrm{T}} \in \mathbb{R}^{2(n+1) \times 1} \tag{57}
\end{equation*}
$$

and consider the vector $P^{h} \in \mathbb{R}^{4(n+1) \times 1}$ of the excitation current source ${ }^{7}$

$$
\begin{equation*}
P^{h}=\left[\left(P_{0}^{h}\right)^{\mathrm{T}}, \quad(0,0,0,0), \quad \ldots, \quad(0,0,0,0)\right]^{\mathrm{T}} \tag{58}
\end{equation*}
$$

Moreover, let the vector

$$
\begin{equation*}
B^{h}=P^{h}+Z^{h} \in \mathbb{R}^{4(n+1) \times 1} \tag{59}
\end{equation*}
$$

where

$$
\begin{equation*}
Z^{h}=\left[\sum_{i, t} Z_{i t j}\right]_{j=0}^{n} \in \mathbb{R}^{4(n+1) \times 1} \tag{60}
\end{equation*}
$$

Then, (56) is written in block-matrix form

$$
\begin{equation*}
B^{h}+\left(G^{h}+K^{h}+X^{h}+\omega F^{h}\right) S^{h}=\left(T^{h}+\omega W^{h}\right) S^{h} \tag{61}
\end{equation*}
$$

[^6]where
\[

$$
\begin{align*}
G^{h}= & G^{f} \otimes I_{2} \in \mathbb{R}^{4(n+1) \times 4(n+1)}  \tag{62}\\
K^{h}= & K^{f} \otimes I_{2} \in \mathbb{R}^{4(n+1) \times 4(n+1)}  \tag{63}\\
F^{h}= & {\left[\widetilde{C}_{\ell j}\right]_{j, \ell=0}^{n} \otimes(L \otimes J) \in \mathbb{R}^{4(n+1) \times 4(n+1)} }  \tag{64}\\
T^{h}= & T^{f} \otimes I_{2} \in \mathbb{R}^{4(n+1) \times 4(n+1)}  \tag{65}\\
W^{h}= & \operatorname{diag}\left(\left[C_{j}+\sum_{\ell} \widetilde{C}_{\ell j}\right]_{j=0}^{n}\right) \otimes(L \otimes J) \in \mathbb{R}^{4(n+1) \times 4(n+1)} \\
X^{h}= & {\left[\sum_{t} X_{i t j}^{\alpha}\right]_{j, i=0}^{n}+\left[\sum_{i} X_{i t j}^{\beta}\right]_{j, t=0}^{n} }  \tag{66}\\
& +\bigoplus_{j=0}^{n}\left[\sum_{i, t} X_{i t j}^{\gamma}\right] \in \mathbb{R}^{4(n+1) \times 4(n+1)} \tag{67}
\end{align*}
$$
\]

and $\bigoplus$ denotes the direct sum of matrices [38].
The solution of (61) gives the vector of the harmonic tones coefficients

$$
\begin{equation*}
S^{h}=\left[T^{h}-G^{h}-K^{h}-X^{h}+\omega\left(W^{h}-F^{h}\right)\right]^{-1} B^{h} . \tag{68}
\end{equation*}
$$

The solution of (43) and (68) can be easily calculated, making the speed and computational efficiency of the proposed method superior to that of traditional distortion estimation via simulation.
Since all coefficients are known, the desired $\mathrm{HD}_{2}$ and $\mathrm{HD}_{3}$ factors can be immediately obtained for any node $j$ of the circuit

$$
\begin{align*}
\mathrm{HD}_{2}^{j} & =10 \log _{10}\left(\frac{a_{j, 2}^{2}+b_{j, 2}^{2}}{a_{j, 1}^{2}+b_{j, 1}^{2}}\right)  \tag{69}\\
\mathrm{HD}_{3}^{j} & =10 \log _{10}\left(\frac{a_{j, 3}^{2}+b_{j, 3}^{2}}{a_{j, 1}^{2}+b_{j, 1}^{2}}\right) . \tag{70}
\end{align*}
$$

For a differential output, between nodes $j$ and $m$, the distortion factors are given by

$$
\begin{align*}
& \mathrm{HD}_{2}^{j m}=10 \log _{10}\left[\frac{\left(a_{j, 2}-a_{m, 2}\right)^{2}+\left(b_{j, 2}-b_{m, 2}\right)^{2}}{\left(a_{j, 1}-a_{m, 1}\right)^{2}+\left(b_{j, 1}-b_{m, 1}\right)^{2}}\right]  \tag{71}\\
& \mathrm{HD}_{3}^{j m}=10 \log _{10}\left[\frac{\left(a_{j, 3}-a_{m, 3}\right)^{2}+\left(b_{j, 3}-b_{m, 3}\right)^{2}}{\left(a_{j, 1}-a_{m, 1}\right)^{2}+\left(b_{j, 1}-b_{m, 1}\right)^{2}}\right] . \tag{72}
\end{align*}
$$

## C. Harmonic Tones Approximation Error Evaluation

The assumption regarding the amplitudes of the harmonic tones that led to the elimination of the products $c$ ) in Section IV-B can be validated retrospectively after the solution of (43) and (68). To this end, consider the error excitation current source of stage $G_{i, t, j}^{m}$

$$
\hat{i}_{i t j}^{\varepsilon}=\theta^{h} \hat{P}_{i t j}^{\varepsilon}=\theta^{h}\left[\begin{array}{llll}
\hat{a}_{i t j, 2}^{\varepsilon}, & 0, & \hat{a}_{i t j, 3}^{\varepsilon}, & 0 \tag{73}
\end{array}\right]^{\mathrm{T}}
$$

where $\hat{P}_{i t j}^{\varepsilon} \in \mathbb{R}^{4 \times 1}$ has the same form as (29) and

$$
\begin{align*}
\hat{a}_{i t j, 2}^{\varepsilon}= & \left|g_{i t j}^{30}\right|\left(\frac{3}{2} \widetilde{c}_{i t j} \widetilde{d}_{i t j}^{2}+\frac{9}{4} \widetilde{d}_{i t j}^{3}\right)+\left|g_{i t j}^{03}\right|\left(\frac{3}{2} c_{j} d_{j}^{2}+\frac{9}{4} d_{j}^{3}\right) \\
& +\left|g_{i t j}^{21}\right|\left(\frac{1}{2} c_{j} \widetilde{d}_{i t j}^{2}+\widetilde{c}_{i t j} \widetilde{d}_{i j} d_{j}+\frac{9}{4} \widetilde{d}_{i t j}^{2} d_{j}\right) \\
& +\left|g_{i t j}^{12}\right|\left(\frac{1}{2} \widetilde{c}_{i t j} d_{j}^{2}+c_{j} \widetilde{d}_{i j} d_{j}+\frac{9}{4} \widetilde{d}_{i j} d_{j}^{2}\right) \tag{74}
\end{align*}
$$

$$
\begin{align*}
\hat{a}_{i t j, 3}^{\varepsilon}= & \left|g_{i t j}^{30}\right|\left(\frac{3}{4} \widetilde{c}_{i t j} \widetilde{d}_{i t j}^{2}+\frac{9}{4} \widetilde{d}_{i t j}^{3}\right)+\left|g_{i t j}^{03}\right|\left(\frac{3}{4} c_{j} d_{j}^{2}+\frac{9}{4} d_{j}^{3}\right) \\
& +\left|g_{i t j}^{21}\right|\left(\frac{1}{4} c_{j} \widetilde{d}_{i t j}^{2}+\frac{1}{2} \widetilde{c}_{i t j} \widetilde{d}_{i t j} d_{j}+\frac{9}{4} \widetilde{d}_{i t j}^{2} d_{j}\right) \\
& +\left|g_{i t j}^{12}\right|\left(\frac{1}{4} \widetilde{c}_{i t j} d_{j}^{2}+\frac{1}{2} c_{j} \widetilde{d}_{i t j} d_{j}+\frac{9}{4} \widetilde{d}_{i t j} d_{j}^{2}\right) \tag{75}
\end{align*}
$$

The coefficients involved in (74) and (75) are the maximum amplitude values of the fundamental and any harmonic tone at the input and the output of stage $G_{i, t, j}^{m}$, over the entire frequency range of interest, evaluated as

$$
\begin{align*}
\widetilde{c}_{i t j} & =\max \left\{\sqrt{\tilde{a}_{i t j, 1}^{2}+\widetilde{b}_{i t j, 1}^{2}}\right\}  \tag{76}\\
c_{j} & =\max \left\{\sqrt{a_{j, 1}^{2}+b_{j, 1}^{2}}\right\}  \tag{77}\\
\widetilde{d}_{i t j} & =\max \left\{\sqrt{\widetilde{a}_{i t j, 2}^{2}+\widetilde{b}_{i t j, 2}^{2}}, \sqrt{\widetilde{a}_{i t j, 3}^{2}+\widetilde{b}_{i t j, 3}^{2}}\right\}  \tag{78}\\
d_{j} & =\max \left\{\sqrt{a_{j, 2}^{2}+b_{j, 2}^{2}}, \sqrt{a_{j, 3}^{2}+b_{j, 3}^{2}}\right\} \tag{79}
\end{align*}
$$

where

$$
\begin{align*}
\widetilde{a}_{i t j, 1} & =a_{i, 1}-k_{i t j} a_{t, 1}  \tag{80}\\
\widetilde{b}_{i t j, 1} & =b_{i, 1}-k_{i t j} b_{t, 1}  \tag{81}\\
\widetilde{a}_{i t j, 2} & =a_{i, 2}-k_{i t j} a_{t, 2}  \tag{82}\\
\widetilde{b}_{i t j, 2} & =b_{i, 2}-k_{i t j} b_{t, 2}  \tag{83}\\
\widetilde{a}_{i t j, 3} & =a_{i, 3}-k_{i t j} a_{t, 3}  \tag{84}\\
\widetilde{b}_{i t j, 3} & =b_{i, 3}-k_{i t j} b_{t, 3} . \tag{85}
\end{align*}
$$

The current of (73) is injected into the output node, $j$, of stage $G_{i, t, j}^{m}$, while no other input is present in the circuit under consideration. The expression of $\hat{i}_{i t j}^{\varepsilon}$ accounts for the products c) that would have been generated by the nonlinear terms of (8). Its magnitude is an overestimation of the induced error in the estimation of the harmonic tones by (68); the maximum values of (76)-(79) may not be an inputoutput corresponding pair in frequency, and may also result from a combination of both harmonic tones. Furthermore, the involved $g_{i t j}^{k \ell}$-coefficients are summed in absolute-value fashion, while they may feature opposite signs.

The harmonic tones coefficients due to the error current source of stage $G_{i, t, j}^{m}$ are given by the solution of

$$
\begin{equation*}
S_{i t j}^{h, \varepsilon}=\left[T^{h}-G^{h}-K^{h}+\omega\left(W^{h}-F^{h}\right)\right]^{-1} P_{i t j}^{h, \varepsilon} \tag{86}
\end{equation*}
$$

where

$$
\begin{gather*}
P_{i t j}^{h, \varepsilon}=\left[\begin{array}{cccc}
(0, & 0, & 0, & 0
\end{array}\right), \ldots, \overbrace{\left(\hat{a}_{i t j, 2}^{\varepsilon},\right.} 0, \hat{a}_{i t j, 3}^{\varepsilon}, 0)
\end{gather*},
$$

This procedure is repeated for all $G_{m}$-stages of the circuit, and a total error estimation is obtained by summing the absolute coefficient values of all $S_{i t j}^{h, \varepsilon} \in \mathbb{R}^{4(n+1) \times 1}$ vectors

$$
\begin{align*}
S^{h, \varepsilon}=\sum_{i t j} & {\left[\left|a_{0,2}^{\varepsilon}\right|,\left|b_{0,2}^{\varepsilon}\right|,\left|a_{0,3}^{\varepsilon}\right|,\left|b_{0,3}^{\varepsilon}\right|\right.} \\
& \left|a_{1,2}^{\varepsilon}\right|,\left|b_{1,2}^{\varepsilon}\right|,\left|a_{1,3}^{\varepsilon}\right|,\left|b_{1,3}^{\varepsilon}\right| \\
& \left.\ldots,\left|a_{n, 2}^{\varepsilon}\right|,\left|b_{n, 2}^{\varepsilon}\right|,\left|a_{n, 3}^{\varepsilon}\right|,\left|b_{n, 3}^{\varepsilon}\right|\right]^{\mathrm{T}} \in \mathbb{R}^{4(n+1) \times 1} . \tag{88}
\end{align*}
$$

Procedure 1: Proposed Harmonic Distortion Estimation
Method
1: Decompose the circuit as an interconnection of CMOS stages.
2: Derive each stage's $g_{i t j}^{k \ell}$-coefficients by curve-fitting, confirming the criteria of Section III-D1 and Section III-D2.
3: Replace each CMOS stage with its $G_{m}$-stage model equivalent.
4: Add all resistors and capacitors, including parasitic ones if of interest.
5: Add the circuit's excitation current source(s).
6: Form matrices (36), (38)-(42) and solve (43) for the fundamental tone estimation.
7: Form matrices (58)-(60), (62)-(67) and solve (68) for the harmonic tones estimation, evaluating the harmonic tones approximation of Section IV-B with the procedure of Section IV-C.
8: Use (69), (70) or (71), (72) to estimate the desired $\mathrm{HD}_{2}$ and $\mathrm{HD}_{3}$ factors.

Again, this constitutes an overestimation of the total propagated error, since (88) supposes all calculated coefficients to be in phase at every node of the circuit.

The final step to validate the harmonic tones approximation is to compare the power of the second and the third harmonic estimated by (68) to the power of the ones resulting from (88), in the frequency range of interest. The power difference would indicate a pessimistic error estimation in the second and the third harmonic, and thus in $\mathrm{HD}_{2}$ and $\mathrm{HD}_{3}$ factors.

## D. Proposed Method Application Procedure

Concluding the theoretical analysis of the harmonic distortion estimation, the application of the proposed method is summarized as Procedure 1.

## E. Application Examples

The application of the proposed method is illustrated via two example cases, where the necessary parameters are derived and all calculation steps are followed. The examples are continued in Section V, where the estimated distortion factors are compared to that resulting by simulation, for a range of frequencies.

1) One-Stage Cascode Amplifier: Consider the one-stage cascode amplifier of Fig. 15. The load, $R_{L} \| C_{L}$, is connected to ground via a dc-voltage of $V_{\mathrm{OUT}}^{o p}$, equal to the dc-voltage of the unloaded output of the amplifier.

The equivalent $G_{m}$-stage representation is shown in Fig. 16, where the whole stage is treated as a single $G_{m}$-stage, $G_{0, r, 1}^{m}$. The ac-input signal, $u_{i n}=u_{0}$, is realized by $\hat{i}_{0}=u_{0} / R_{0}$ acting on $R_{0}=1 \Omega$, and $R_{L}=R_{1}, C_{L}=C_{1}$.

Given the stage's derived $g_{0 r 1}^{k \ell}$-coefficients, the necessary matrices are defined according to (35), (36), and (38)-(42)

$$
\begin{aligned}
S^{f} & =\left[\begin{array}{llll}
a_{0,1}, & b_{0,1}, & a_{1,1}, & b_{1,1}
\end{array}\right]^{\mathrm{T}} \\
P^{f} & =\left[\begin{array}{lll}
\hat{a}_{0,1}, & \hat{b}_{0,1}, & 0,
\end{array}\right]^{\mathrm{T}}
\end{aligned}
$$



Fig. 15. One-stage cascode amplifier.


Fig. 16. $G_{m}$-stage equivalent representation of the one-stage cascode amplifier.
and

$$
\begin{aligned}
G^{f} & =\left[\begin{array}{cc}
0 & 0 \\
g_{0 r 1}^{10} & 0
\end{array}\right] \otimes I_{2} \\
K^{f} & =0_{4} \\
F^{f} & =0_{4} \\
T^{f} & =\left[\begin{array}{cc}
\frac{1}{R_{0}} & 0 \\
0 & \frac{1}{R_{1}}-g_{0 r 1}^{01}
\end{array}\right] \otimes I_{2} \\
W^{f} & =\left[\begin{array}{cc}
0 & 0 \\
0 & C_{1}
\end{array}\right] \otimes J
\end{aligned}
$$

where $O_{n}$ denotes the $n \times n$ zero matrix. Then, the solution for the fundamental tone coefficients comes directly from (43).

The harmonic tones coefficients vector, $S^{h}$, and the vector $B^{h}$ are defined according to (57)-(60)

$$
\begin{aligned}
& S^{h}=\left[\begin{array}{llllll}
a_{0,2} & b_{0,2}, & a_{0,3} & b_{0,3}, & a_{1,2} & b_{1,2}, \\
a_{1,3} & b_{1,3}
\end{array}\right]^{\mathrm{T}} \\
& B^{h}=\left[\hat{a}_{0,2}, \quad \hat{b}_{0,2}, \hat{a}_{0,3}, \hat{b}_{0,3}, p_{0 r 1}^{s 2}, p_{0 r 1}^{c 2}, p_{0 r 1}^{s 3}, p_{0 r 1}^{c 3}\right]^{\mathrm{T}}
\end{aligned}
$$

where the following are calculated using (46)-(49)

$$
\begin{aligned}
p_{0 r 1}^{s 2} & =g_{0 r 1}^{20} \tilde{f}_{0 r 1}^{s 2}+g_{0 r 1}^{02} f_{1}^{s 2}+g_{0 r 1}^{11} h_{0 r 1}^{s 2} \\
p_{0 r 1}^{c 2} & =g_{0 r 1}^{20} \tilde{f}_{0 r 1}^{c 2}+g_{0 r 1}^{02} f_{1}^{c 2}+g_{0 r 1}^{11} h_{0 r 1}^{c 2} \\
p_{0 r 1}^{s 3} & =g_{0 r 1}^{30} \widetilde{f}_{0 r 1}^{s 3}+g_{0 r 1}^{03} f_{1}^{s 3}+g_{0 r 1}^{21} r_{0 r 1}^{s 3}+g_{0 r 1}^{12} s_{0 r 1}^{s 3} \\
p_{0 r 1}^{c 3} & =g_{0 r 1}^{30} \tilde{f}_{0 r 1}^{c 3}+g_{0 r 1}^{03} f_{1}^{c 3}+g_{0 r 1}^{21} r_{0 r 1}^{c 3}+g_{0 r 1}^{12} o_{0 r 1}^{c 3}
\end{aligned}
$$

and (89)-(102) are calculated for $\widetilde{a}_{0 r 1,1}=a_{0,1}, \widetilde{b}_{0 r 1,1}=b_{0,1}$, $a_{1,1}$, and $b_{1,1}$. Finally, following (50)-(52) and (62)-(67), it is

$$
\begin{aligned}
G^{h} & =G^{f} \otimes I_{2} \\
K^{h} & =0_{8}
\end{aligned}
$$

$$
\begin{aligned}
F^{h} & =o_{8} \\
T^{h} & =T^{f} \otimes I_{2} \\
W^{h} & =\left[\begin{array}{cc}
0 & 0 \\
0 & C_{1}
\end{array}\right] \otimes(L \otimes J) \\
X^{h} & =\left[\begin{array}{cc}
O_{4} & O_{4} \\
X_{0 r 1}^{\alpha} & X_{0 r 1}^{\gamma}
\end{array}\right]
\end{aligned}
$$

where

$$
\begin{aligned}
X_{0 r 1}^{\alpha}= & g_{0 r 1}^{20} \widetilde{N}_{0 r 1}+g_{0 r 1}^{30} \frac{3}{2} \widetilde{M}_{0 r 1}+g_{0 r 1}^{11} \frac{1}{2} N_{1}+g_{0 r 1}^{21} Q_{0 r 1} \\
& +g_{0 r 1}^{12} \frac{1}{2} M_{1} \\
X_{0 r 1}^{\gamma}= & g_{0 r 1}^{02} N_{1}+g_{0 r 1}^{03} \frac{3}{2} M_{1}+g_{0 r 1}^{11} \frac{1}{2} \widetilde{N}_{0 r 1}+g_{0 r 1}^{21} \frac{1}{2} \widetilde{M}_{0 r 1} \\
& +g_{0 r 1}^{12} Q_{0 r 1}
\end{aligned}
$$

and the matrices of (103)-(107) are similarly calculated for $\tilde{a}_{0 r 1,1}=a_{0,1}, b_{0 r 1,1}=b_{0,1}, a_{1,1}$, and $b_{1,1}$. The harmonic tones solution is given by (68), enabling the estimation of $\mathrm{HD}_{2}$ and $\mathrm{HD}_{3}$ at the amplifier's output.
2) Three-Stage Feedback Amplifier: A more general example is given for the three-stage feedback amplifier of Fig. 9. The application of the method on the equivalent network of Fig. 10 yields the following matrices according to the definitions of (38)-(42)

$$
\begin{aligned}
G^{f} & =\left[\begin{array}{ccccc}
0 & 0 & 0 & 0 & 0 \\
g_{031}^{10} & 0 & 0 & 0 & 0 \\
0 & g_{2 r 1}^{10} & 0 & 0 & 0 \\
0 & 0 & g_{233}^{10} & 0 & 0 \\
0 & 0 & g_{244}^{10} & 0 & 0
\end{array}\right] \otimes I_{2} \\
K^{f} & =\left[\begin{array}{ccccc}
0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & -g_{031}^{10} k_{031} & 0 \\
0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & -g_{233}^{10} & 0 \\
0 & 0 & 0 & 0 & -g_{244}^{10}
\end{array}\right] \otimes I_{2} \\
F^{f} & =\left[\begin{array}{ccccc}
0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & \widetilde{C}_{14} \\
0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 \\
0 & \widetilde{C}_{14} & 0 & 0 & 0
\end{array}\right] \otimes J \\
T^{f} & =\left[\begin{array}{ccccc}
\frac{1}{R_{0}} & 0 & 0 & 0 & 0 \\
0 & -g_{031}^{01} & 0 & 0 & 0 \\
0 & 0 & -g_{1 r 2}^{01} \\
0 & 0 & 0 & \frac{1}{R_{3}}-g_{233}^{01} & 0 \\
0 & 0 & 0 & 0 & -g_{244}^{01}
\end{array}\right] \otimes I_{2} \\
W^{f} & =\left[\begin{array}{lllll}
0 & 0 & 0 & 0 & 0 \\
0 & \widetilde{C}_{14} & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & C_{3} & 0 \\
0 & 0 & 0 & 0 & \widetilde{C}_{14}
\end{array}\right] \otimes J .
\end{aligned}
$$

The vectors $S^{f}$ and $P^{f}$ of (35) and (36) are omitted for simplicity. Then, (43) gives the fundamental tone coefficients for all nodes of the circuit.

For the harmonic tones estimation, the matrices of (62)-(67) are

$$
\begin{aligned}
& G^{h}=G^{f} \otimes I_{2} \\
& K^{h}=K^{f} \otimes I_{2}
\end{aligned}
$$

$$
\begin{aligned}
F^{h} & =\left[\begin{array}{ccccc}
0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & \widetilde{C}_{14} \\
0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 \\
0 & \widetilde{C}_{14} & 0 & 0 & 0
\end{array}\right] \otimes(L \otimes J) \\
T^{h} & =T^{f} \otimes I_{2} \\
W^{h} & =\left[\begin{array}{ccccc}
0 & 0 & 0 & 0 & 0 \\
0 & \widetilde{C}_{14} & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & C_{3} & 0 \\
0 & 0 & 0 & 0 & \widetilde{C}_{14}
\end{array}\right] \otimes(L \otimes J) \\
X^{h} & =\left[\begin{array}{ccccc}
0_{4} & 0_{4} & 0_{4} & O_{4} & O_{4} \\
X_{031}^{\alpha} & X_{031}^{\gamma} & O_{4} & X_{031}^{\beta} & O_{4} \\
0_{4} & X_{1 r 2}^{\alpha} & X_{1 r 2}^{\gamma} & O_{4} & O_{4} \\
0_{4} & O_{4} & X_{233}^{\alpha} & X_{233}^{\beta}+X_{233}^{\gamma} & O_{4} \\
O_{4} & O_{4} & X_{244}^{\alpha} & O_{4} & X_{244}^{\beta}+X_{244}^{\gamma}
\end{array}\right]
\end{aligned}
$$

where the following are calculated using (50)-(52), the corresponding (80), (81), and (103)-(107)

$$
\begin{aligned}
X_{031}^{\alpha}= & g_{031}^{20} \widetilde{N}_{031}+g_{031}^{30} \frac{3}{2} \widetilde{M}_{031}+g_{031}^{11} \frac{1}{2} N_{1}+g_{031}^{21} Q_{031} \\
& +g_{031}^{12} \frac{1}{2} M_{1} \\
X_{031}^{\beta}= & -k_{031} X_{031}^{\alpha} \\
X_{031}^{\gamma}= & g_{031}^{02} N_{1}+g_{031}^{03} \frac{3}{2} M_{1}+g_{031}^{11} \frac{1}{2} \widetilde{N}_{031}+g_{031}^{21} \frac{1}{2} \widetilde{M}_{031} \\
& +g_{031}^{12} Q_{031} \\
X_{1 r 2}^{\alpha}= & g_{1 r 2}^{20} \widetilde{N}_{1 r 2}+g_{1 r 2}^{30} \frac{3}{2} \widetilde{M}_{1 r 2}+g_{1 r 2}^{11} \frac{1}{2} N_{2}+g_{1 r 2}^{21} Q_{1 r 2} \\
& +g_{1 r 2}^{12} \frac{1}{2} M_{2} \\
X_{1 r 2}^{\gamma}= & g_{1 r 2}^{02} N_{2}+g_{1 r 2}^{03} \frac{3}{2} M_{2}+g_{1 r 2}^{11} \frac{1}{2} \widetilde{N}_{1 r 2}+g_{1 r 2}^{21} \frac{1}{2} \widetilde{M}_{1 r 2} \\
& +g_{1 r 2}^{12} Q_{1 r 2} \\
X_{233}^{\alpha}= & g_{233}^{20} \widetilde{N}_{233}+g_{233}^{30} \frac{3}{2} \widetilde{M}_{233}+g_{233}^{11} \frac{1}{2} N_{3}+g_{233}^{21} Q_{233} \\
& +g_{233}^{12} \frac{1}{2} M_{3} \\
X_{233}^{\beta}= & -X_{233}^{\alpha} \\
X_{233}^{\gamma}= & g_{233}^{02} N_{3}+g_{233}^{03} \frac{3}{2} M_{3}+g_{233}^{11} \frac{1}{2} \widetilde{N}_{233}+g_{233}^{21} \frac{1}{2} \widetilde{M}_{233} \\
& +g_{233}^{12} Q_{233} \\
X_{244}^{\alpha}= & g_{244}^{20} \widetilde{N}_{244}+g_{244}^{30} \frac{3}{2} \widetilde{M}_{244}+g_{244}^{11} \frac{1}{2} N_{4}+g_{244}^{21} Q_{244} \\
& +g_{244}^{12} \frac{1}{2} M_{4} \\
X_{244}^{\beta}= & -X_{244}^{\alpha} \\
X_{244}^{\gamma}= & g_{244}^{02} N_{4}+g_{244}^{03} \frac{3}{2} M_{4}+g_{244}^{11} \frac{1}{2} \widetilde{N}_{244}+g_{244}^{21} \frac{1}{2} \widetilde{M}_{244} \\
& +g_{244}^{12} Q_{244 .}
\end{aligned}
$$

The vectors $S^{h}$ and $B^{h}$ of (57) and (59) (omitted for simplicity) are formed according to the definitions of (46)-(49), (58), and (60), the corresponding (80), (81), and (89)-(102). The harmonic tones coefficients are finally computed by (68), and thus $\mathrm{HD}_{2}$ and $\mathrm{HD}_{3}$.


Fig. 17. Resistor as two antiparallel $G_{m}$-stages.

## F. Additional Remarks

To conclude the proposed method's core section, some remarks regarding less typical circuit cases are in order.

1) Series Resistor: In a case where a resistor is in series with a capacitor (like in a Miller compensation scheme where a series resistor is included for the elimination of a possible right half-plane zero), the method's form does not feature an explicit way to account for it. However, the resistor can be replaced by the equivalent block of Fig. 17, as a $G_{m}$-stage model. If the resistor, $R$, placed between nodes $a$ and $b$, is linear, the only nonzero coefficients of the two stages modeled by (8) are $g_{b a a}^{10}=g_{a b b}^{10}=1 / R$. Weak nonlinear behavior of a resistor can also be captured by including more terms of (8).
Another possible use of such a resistor formulation is to model a capacitance, $C_{t}$, at the negative input of the stage $G_{i, t, j}^{m}$, in the case where $k_{i t j} \neq 1$. For example, if a capacitor is added at the gate of $M_{2}$ in Fig. $9, R_{L_{1}}$ can be replaced by the equivalent block of Fig. 17; then, the voltage at the gate of $M_{2}$ becomes a new variable, $u_{5}$, and stage $G_{0,3,1}^{m}$ changes to $G_{0,5,1}^{m}$, with $k_{051}=1$.
2) Parasitic Capacitors: Parasitic capacitors can be added at any node of the $G_{m}$-stage circuit representation. Their inclusion benefits nodes where no other capacitance is present, or nodes with a very small capacitance value.
3) Process, Supply Voltage, and Temperature Variations: The method can be performed for process, supply voltage, and temperature variations (PVT), by simply repeating the derivation of the $g_{i t i}^{k \ell}$-coefficients of all $G_{m}$-stages and then solving (43) and (68). The variations will have an impact on the resulting output currents of the stages, that will be captured by the newly derived $g_{i t j}^{k \ell}$-coefficients. The repetition of the method is much faster than the required repetition of simulation, boosting even further the speed-up gain of the distortion estimation.

## V. Simulation Results

The proposed harmonic distortion estimation method is verified by Cadence Spectre simulation in TSMC $0.18 \mu \mathrm{~m}$ technology. Supply rails are set to $\pm 2.5 \mathrm{~V}$ for all cases presented, while distortion results are obtained from parametric PSS-analysis. Each amplifier load has a reference voltage equal to the dc-operating point of its output node.
The method is implemented in MATLAB, and no parasitic capacitors are taken into account; their inclusion would result in better accuracy at high frequencies. As a rough estimate, the time of distortion estimation drops from minutes (parametric PSS-analysis) to seconds (proposed method).

## A. One-Stage Cascode Amplifier

The first simulation case is that of the one-stage cascode amplifier of Fig. 15. The implemented stage has 21.56 dB dcgain and a unity-gain frequency of 17.86 MHz , while driving a load of $10 \mathrm{k} \Omega \| 10 \mathrm{pF}$. Under an input signal of 12.5 mV peak,


Fig. 18. $\mathrm{HD}_{2}$ of the one-stage cascode amplifier.


Fig. 19. $\mathrm{HD}_{3}$ of the one-stage cascode amplifier.
the method's resulting $\mathrm{HD}_{2}$ and $\mathrm{HD}_{3}$ factors are presented in Figs. 18 and 19, respectively, against the results obtained by Cadence Spectre.

The results of the proposed method are in fine agreement with the simulation ones; the error between the simulation and the method is less than 0.64 dB for $\mathrm{HD}_{2}$, and 1.75 dB for $\mathrm{HD}_{3}$, for up to 100 MHz . For frequencies up to the unity-gain frequency of the amplifier, the $\mathrm{HD}_{2}$ error is found to be less than 0.33 dB .

## B. Three-Stage Feedback Amplifier

Next, the method is applied to the three-stage feedback amplifier of Fig. 9, that has a feedback factor of $\beta=0.05$. The amplifier drives a load of $10 \mathrm{k} \Omega \| 10 \mathrm{pF}$, has a dc-gain of 26.01 dB and a unity-gain frequency of 16.80 MHz . Fig. 20 shows $\mathrm{HD}_{2}$, with $\mathrm{HD}_{3}$ being depicted in Fig. 21, for an input signal of 25 mV peak.
Both distortion factors predicted by the method are in excellent agreement with the ones from the simulation. Up to 100 MHz , the error is less than 1.79 dB for $\mathrm{HD}_{2}$, and less than 1.58 dB for $\mathrm{HD}_{3}$. Within the amplifier's unity-gain bandwidth, the maximum errors fall to 0.49 dB and 0.93 dB , respectively.

## C. One-Stage Fully-Differential Amplifier

Finally, the one-stage fully-differential amplifier of Fig. 22 is simulated. The structure has 25.52 dB dc-gain and 23.79 MHz unity-gain frequency, under a load of $25 \mathrm{k} \Omega \| 5 \mathrm{pF}$ per output. With a 25 mV peak input signal, Figs. 23 and 24 present $\mathrm{HD}_{2}$ and $\mathrm{HD}_{3}$, respectively.

The $\mathrm{HD}_{3}$ factor is again in fine agreement with the corresponding simulation result; the error is found to be less than 2.36 dB for the bandwidth of 100 MHz , while within the amplifier's unity-gain frequency is less than 0.41 dB . Both


Fig. 20. $\mathrm{HD}_{2}$ of the three-stage feedback amplifier.


Fig. 21. $\mathrm{HD}_{3}$ of the three-stage feedback amplifier.


Fig. 22. One-stage fully-differential amplifier.
$\mathrm{HD}_{2}$ estimation results indicate practically a negligible second harmonic, as is expected by the fully-differential nature of the structure.

## VI. Conclusion

In this article, a time-domain harmonic distortion estimation method is presented, that can be applied systematically to CMOS circuits with any number of stages. It can be implemented in MATLAB and yields accurate distortion estimation


Fig. 23. $\mathrm{HD}_{2}$ of the one-stage fully-differential amplifier.


Fig. 24. $\mathrm{HD}_{3}$ of the one-stage fully-differential amplifier.
results, verified by comparison with Cadence Spectre simulation. The method holds a fast computational profile that intends to be exploited by integration in EDA suites.

## Appendix

With coefficients $\tilde{a}_{i t j, 1}, \widetilde{b}_{i t j, 1}, a_{j, 1}$, and $b_{j, 1}$ known from the solution of $S^{f}$ by (43), the coefficients of (46)-(49) and the matrices (103)-(107) of (50)-(52) are given by

$$
\begin{align*}
\widetilde{f}_{i t j}^{s 2} & =\widetilde{a}_{i t j, 1} \tilde{b}_{i t j, 1}  \tag{89}\\
\widetilde{f}_{i t j}^{c 2} & =\frac{-\widetilde{a}_{i t j, 1}^{2}+\widetilde{b}_{i t j, 1}^{2}}{2}  \tag{90}\\
\widetilde{f}_{i t j}^{s 3} & =\frac{\widetilde{a}_{i t j, 1}}{4}\left(-\widetilde{a}_{i t j, 1}^{2}+3 \widetilde{b}_{i t j, 1}^{2}\right)  \tag{91}\\
\widetilde{f}_{i t j}^{c 3} & =\frac{\widetilde{b}_{i t j, 1}}{4}\left(-3 \widetilde{a}_{i t j, 1}^{2}+\widetilde{b}_{i t j, 1}^{2}\right)  \tag{92}\\
f_{j}^{s 2} & =a_{j, 1} b_{j, 1}  \tag{93}\\
f_{j}^{c 2} & =\frac{-a_{j, 1}^{2}+b_{j, 1}^{2}}{2}  \tag{94}\\
f_{j}^{s 3} & =\frac{a_{j, 1}}{4}\left(-a_{j, 1}^{2}+3 b_{j, 1}^{2}\right)  \tag{95}\\
f_{j}^{c 3} & =\frac{b_{j, 1}}{4}\left(-3 a_{j, 1}^{2}+b_{j, 1}^{2}\right)  \tag{96}\\
h_{i t j}^{s 2} & =\frac{\widetilde{a}_{i t j, 1} b_{j, 1}+\widetilde{b}_{i t j, 1} a_{j, 1}}{2}  \tag{97}\\
h_{i t j}^{c 2} & =\frac{-\widetilde{a}_{i t j, 1} a_{j, 1}+\widetilde{b}_{i t j, 1} b_{j, 1}}{2}  \tag{98}\\
r_{i t j}^{s 3} & =\left(-\widetilde{a}_{i t j, 1}^{2}+\widetilde{b}_{i t j, 1}^{2}\right) \frac{a_{j, 1}}{4}+\left(\widetilde{a}_{i t j, 1} \widetilde{b}_{i t j, 1}\right) \frac{b_{j, 1}}{2}  \tag{99}\\
r_{i t j}^{c 3} & =\left(-\widetilde{a}_{i t j, 1}^{2}+\widetilde{b}_{i j t, 1}^{2}\right) \frac{b_{j, 1}}{4}-\left(\widetilde{a}_{i t j, 1} \widetilde{b}_{i t j, 1}\right) \frac{a_{j, 1}}{2} \tag{100}
\end{align*}
$$

$$
\begin{align*}
o_{i t j}^{s 3} & =\left(-a_{j, 1}^{2}+b_{j, 1}^{2}\right) \frac{\tilde{a}_{i t j, 1}}{4}+\left(a_{j, 1} b_{j, 1}\right) \frac{\widetilde{b}_{i t j, 1}}{2}  \tag{101}\\
o_{i t j}^{c 3} & =\left(-a_{j, 1}^{2}+b_{j, 1}^{2}\right) \frac{\widetilde{b}_{i t j, 1}}{4}-\left(a_{j, 1} b_{j, 1}\right) \frac{\widetilde{a}_{i t j, 1}}{2}  \tag{102}\\
\widetilde{N}_{i t j} & =\left[\begin{array}{cccc}
0 & 0 & \widetilde{b}_{i t j, 1} & -\widetilde{a}_{i t j, 1} \\
0 & 0 & \widetilde{a}_{i t j, 1} & \widetilde{b}_{i t j, 1} \\
\widetilde{b}_{i t j, 1} & \widetilde{a}_{i t j, 1} & 0 & 0 \\
-\widetilde{a}_{i t j, 1} & \widetilde{b}_{i t j, 1} & 0 & 0
\end{array}\right] \in \mathbb{R}^{4 \times 4}  \tag{103}\\
N_{j} & =\left[\begin{array}{cccc}
0 & 0 & b_{j, 1} & -a_{j, 1} \\
0 & 0 & a_{j, 1} & b_{j, 1} \\
b_{j, 1} & a_{j, 1} & 0 & 0 \\
-a_{j, 1} & b_{j, 1} & 0 & 0
\end{array}\right] \in \mathbb{R}^{4 \times 4}  \tag{104}\\
\widetilde{M}_{i t j} & =\left(\widetilde{a}_{i t j, 1}^{2}+\widetilde{b}_{i t j, 1}^{2}\right) I_{4} \in \mathbb{R}^{4 \times 4}  \tag{105}\\
M_{j} & =\left(a_{j, 1}^{2}+b_{j, 1}^{2}\right) I_{4} \in \mathbb{R}^{4 \times 4}  \tag{106}\\
Q_{i t j} & =\left(\widetilde{a}_{i t j, 1} a_{j, 1}+\widetilde{b}_{i t j, 1} b_{j, 1}\right) I_{4} \in \mathbb{R}^{4 \times 4} . \tag{107}
\end{align*}
$$

## REFERENCES

[1] T. C. Carusone, D. Johns, and K. Martin, Analog Integrated Circuit Design, 2nd ed. Hoboken, NJ, USA: Wiley, 2011.
[2] D. Self, Audio Power Amplifier Design, 6th ed. New York, NY, USA: Focal Press, 2013.
[3] P. Dobrovolny, G. Vandersteen, P. Wambacq, and S. Donnay, "Analysis and compact behavioral modeling of nonlinear distortion in analog communication circuits," IEEE Trans. Comput.-Aided Design Integr. Circuits Syst., vol. 22, no. 9, pp. 1215-1227, Sep. 2003.
[4] R. J. Gilmore and M. B. Steer, "Nonlinear circuit analysis using the method of harmonic balance-A review of the art. Part I. Introductory concepts," Int. J. Microw. Millimeter Wave Comput.-Aided Eng., vol. 1, no. 1, pp. 22-37, 1991.
[5] S. A. Maas, Nonlinear Microwave and RF Circuits, 2nd ed. Boston, MA, USA: Artech House, 2003.
[6] D. Tannir and R. Khazaka, "Adjoint sensitivity analysis of nonlinear distortion in radio frequency circuits," IEEE Trans. Comput.-Aided Design Integr. Circuits Syst., vol. 30, no. 6, pp. 934-939, Jun. 2011.
[7] D. Tannir, "Direct sensitivity analysis of nonlinear distortion in RF circuits using multidimensional moments," IEEE Trans. Comput.-Aided Design Integr. Circuits Syst., vol. 34, no. 3, pp. 321-331, Mar. 2015.
[8] P. Wambacq and W. Sansen, Distortion Analysis of Analog Integrated Circuits (The Springer International Series in Engineering and Computer Science), 1st ed. Boston, MA, USA: Springer, 1998, vol. 451.
[9] P. Li and L. T. Pileggi, "Efficient per-nonlinearity distortion analysis for analog and RF circuits," IEEE Trans. Comput.-Aided Design Integr. Circuits Syst., vol. 22, no. 10, pp. 1297-1309, Oct. 2003.
[10] Z. Zhang, A. Celik, and P. Sotiriadis, "A fast state-space algorithm to estimate harmonic distortion in fully differential weakly nonlinear $g_{m}-c$ filters," in Proc. IEEE Int. Symp. Circuits Syst. (ISCAS), Kos, Greece, May 2006, pp. 2954-2956.
[11] Z. Zhang, A. Celik, and P. P. Sotiriadis, "State-space harmonic distortion modeling in weakly nonlinear, fully balanced $g_{m}-c$ filters-a modular approach resulting in closed-form solutions," IEEE Trans. Circuits Syst. I. Reg. Papers, vol. 53, no. 1, pp. 48-59, Jan. 2006.
[12] P. P. Sotiriadis, A. Celik, D. Loizos, and Z. Zhang, "Fast state-space harmonic-distortion estimation in weakly nonlinear $g_{m}-c$ filters," IEEE Trans. Circuits Syst. I, Reg. Papers, vol. 54, no. 1, pp. 218-228, Jan. 2007.
[13] A. Cooman, P. Bronders, D. Peumans, G. Vandersteen, and Y. Rolain, "Distortion contribution analysis with the best linear approximation," IEEE Trans. Circuits. Syst. I, Reg. Papers, vol. 65, no. 12, pp. 4133-4146, Dec. 2018.
[14] Y. Miao and Y. Zhang, "Distortion modeling of feedback two-stage amplifier compensated with miller capacitor and nulling resistor," IEEE Trans. Circuits Syst. I, Reg. Papers, vol. 59, no. 1, pp. 93-105, Jan. 2012.
[15] G. Shi, "Symbolic distortion analysis of multistage amplifiers," IEEE Trans. Circuits Syst. I, Reg. Papers, vol. 66, no. 1, pp. 369-382, Jan. 2019.
[16] G. Palumbo and S. Pennisi, "Feedback amplifiers: A simplified analysis of harmonic distortion in the frequency domain," in Proc. 8th IEEE Int. Conf. Electron. Circuits Syst. (Cat.No.01EX483 ), vol. 1. Valletta, Malta, Sep. 2001, pp. 209-212.
[17] G. Giustolisi, G. Palumbo, and S. Pennisi, "Harmonic distortion in single-stage amplifiers," in Proc. IEEE Int. Symp. Circuits Syst. (Cat. No.02CH37353), vol. 2. Scottsdale, AZ, USA, May 2002, pp. 33-36.
[18] G. Palumbo and S. Pennisi, "High-frequency harmonic distortion in feedback amplifiers: Analysis and applications," IEEE Trans. Circuits Syst. I, Fundam. Theory Appl., vol. 50, no. 3, pp. 328-340, Mar. 2003.
[19] G. Palumbo and S. Pennisi, "Harmonic distortion in three-stage nested-miller-compensated amplifiers," in Proc. IEEE Int. Symp. Circuits Syst. (IEEE Cat. No.04CH37512), vol. 1. Vancouver, BC, Canada, May 2004, pp. 485-488.
[20] S. O. Cannizzaro, G. Palumbo, and S. Pennisi, "Accurate estimation of high-frequency harmonic distortion in two-stage miller OTAs," IEE Proc. Circuits Devices Syst., vol. 152, no. 5, pp. 417-424, Oct. 2005.
[21] S. O. Cannizzaro, G. Palumbo, and S. Pennisi, "Distortion analysis of three-stage amplifiers with reversed nested-miller compensation," in Proc. Eur. Conf. Circuit Theory Design, vol. 3. Cork, Ireland, Sep. 2005, pp. III/93-III/96.
[22] S. O. Cannizzaro, G. Palumbo, and S. Pennisi, "New analytical approach to evaluate harmonic distortion in nonlinear feedback amplifiers," in Proc. Eur. Conf. Circuit Theory Design, vol. 3. Cork, Ireland, Sep. 2005, pp. III/97-III/100.
[23] S. O. Cannizzaro, G. Palumbo, and S. Pennisi, "Distortion analysis of miller-compensated three-stage amplifiers," IEEE Trans. Circuits Syst. I, Reg. Papers, vol. 53, no. 5, pp. 961-976, May 2006.
[24] S. O. Cannizzaro, G. Palumbo, and S. Pennisi, "An approach to model high-frequency distortion in negative-feedback amplifiers," Int. J. Circuit Theory Appl., vol. 36, no. 1, pp. 3-18, 2008.
[25] A. Borys, "On analysis of harmonic distortion in op amps based circuits via volterra series," in Proc. 23rd Int. Conf. Mixed Design Integr. Circuits Syst. (MIXDES), Lodz, Poland, Jun. 2016, pp. 330-335.
[26] D. Baxevanakis and P. P. Sotiriadis, "Accurate harmonic distortion estimation in CMOS circuits using a cross-product $g_{m}$-stage modeling," in Proc. IEEE Int. Symp. Circuits Syst. (ISCAS), 2020, pp. 1-5.
[27] S. Kang, B. Choi, and B. Kim, "Linearity analysis of CMOS for RF application," IEEE Trans. Microw. Theory Techn., vol. 51, no. 3, pp. 972-977, Mar. 2003.
[28] B. Toole, C. Plett, and M. Cloutier, "RF circuit implications of moderate inversion enhanced linear region in mosfets," IEEE Trans. Circuits Syst. I, Reg. Papers, vol. 51, no. 2, pp. 319-328, Feb. 2004.
[29] B. Hernes and W. Sansen, "Distortion in single-, two- and three-stage amplifiers," IEEE Trans. Circuits Syst. I, Reg. Papers, vol. 52, no. 5, pp. 846-856, May 2005.
[30] S. C. Blaakmeer, E. A. M. Klumperink, D. M. W. Leenaerts, and B. Nauta, "Wideband Balun-LNA with simultaneous output balancing, noise-canceling and distortion-canceling," IEEE J. Solid-State Circuits, vol. 43, no. 6, pp. 1341-1350, Jun. 2008.
[31] P. S. Crovetti, "Finite common-mode rejection in fully differential nonlinear circuits," IEEE Trans. Circuits Syst. II, Exp. Briefs, vol. 58, no. 8, pp. 507-511, Aug. 2011.
[32] P. S. Crovetti and F. Fiori, "Finite common-mode rejection in fully differential operational amplifiers," Electron. Lett., vol. 42, no. 11, pp. 615-617, May 2006.
[33] F. Fiori and P. S. Crovetti, "Nonlinear effects of radio-frequency interference in operational amplifiers," IEEE Trans. Circuits Syst. I, Fundam. Theory Appl., vol. 49, no. 3, pp. 367-372, Mar. 2002.
[34] B. Perez-Verdu, J. Cruz, B. Linares-Barranco, A. Rodriguez-Vazquez, J. L. Huertas, and E. Sanchez-Sinencio, "Nonlinear time-domain macromodeling of OTA circuits," in Proc. IEEE Int. Symp. Circuits Syst., vol. 2. Portland, OR, USA, May 1989, pp. 1441-1444.
[35] N. Scheinberg and A. Pinkhasov, "A computer simulation model for simulating distortion in FET resistors," IEEE Trans. Comput.-Aided Design Integr. Circuits Syst., vol. 19, no. 9, pp. 981-989, Sep. 2000.
[36] H. W. Sorenson, Parameter Estimation: Principles and Problems (Control and Systems Theory), vol. 9. New York, NY, USA: M. Dekker, 1980.
[37] P. Lancaster and M. Tismenetsky, The Theory of Matrices With Applications (Computer Science and Scientific Computing), 2nd ed. New York, NY, USA: Academic, 1985.
[38] R. A. Horn and C. R. Johnson, Matrix Analysis, 2nd ed. New York, NY, USA: Cambridge Univ. Press, 2013.


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[^1]:    ${ }^{1}$ This particular notation is adopted to capture a range of popular topologies.
    ${ }^{2}$ Commas between $i, t$, and $j$ are omitted in coefficients, voltages, and currents for simplicity.

[^2]:    ${ }^{3}$ Including higher-order terms will account better for distortion expansion and compression at the harmonics of interest; however, since weak nonlinearities are assumed, the error will be negligible.

[^3]:    ${ }^{4}$ If the input signal source drives a CG stage, $R_{0}$ must be significantly smaller in value than the input impedance of the stage.

[^4]:    ${ }^{5}$ Vectors $\theta^{f}$ and $\theta^{h}$ are functions of time, so a more appropriate notation would be that of $\theta^{f}(t)$ and $\theta^{h}(t)$; time is omitted for simplicity.

[^5]:    ${ }^{6}$ If more than one excitation signals are present, they should be included in the corresponding entries of the vector.

[^6]:    ${ }^{7}$ In the case of more than one excitation signals being present, they should be added at the corresponding entries of the vector.

